

Fig. 1

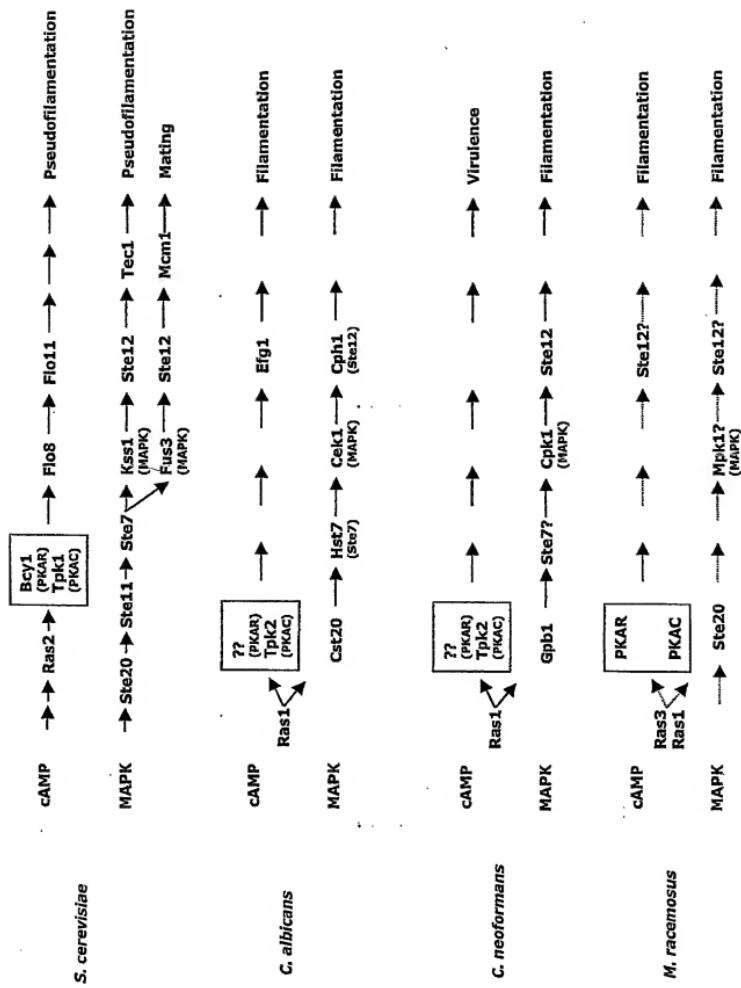


Fig. 2

Fig. 3

Fig. 4

Fig. 5

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Fig. 6a

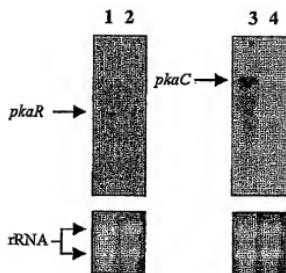
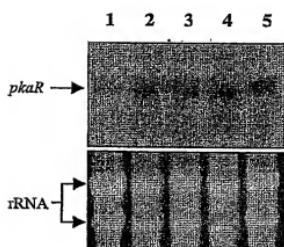


Fig. 6b



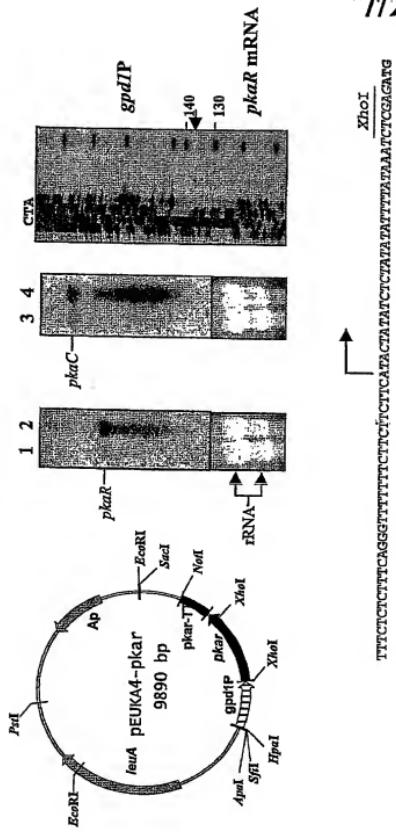


Fig. 7a

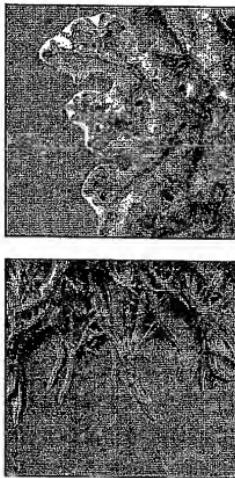


Fig. 7b

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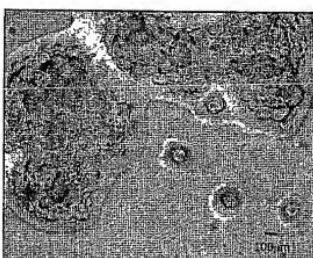
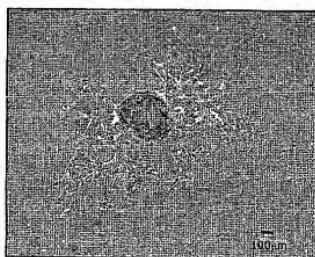


Fig. 8

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Fig. 9a

1	KPF1LATA1PNEB1KPLGKFA	PGGAAAAPGGRBEPNSGSE1SC1LNLHIFITGTD1VRSUT1RFAHGFVW1NARK	MciR
44	KPF1LATA1UNWEN1KPLRYYI	NSGCGFVSCVPRNLL1MT1GTD1VRSUT1RFAHGFVW1NARK	Scen
33	KPF1LATA1PANGW1QVIRYYI	NDDEGFVSCVPRNLL1MT1GTD1VRSUT1RFAHGFVW1NARK	Calin
43	KPF1LATA1PANGW1QVIRYYI	NDDEGFVSCVPRNLL1MT1GTD1VRSUT1RFAHGFVW1NARK	Klack
43	KPF1LATA1PANGW1QVIRYYI	TGDI1SCV1ENP1HFG1SGD1VCLR1F0R1A1G1H1K1NSK1	Anid
19	KPF1LATA1PANWENQ1VIRYYI	NDDEGFVSCVPRNLL1MT1GTD1VRSUT1RFAHGFVW1NARK	Cpu
81	REG1G1FSLDRN1L1KHD1P1L1BEPKSF1L1M1Y1P1F1R1T1O1K1Q1K1R1		MciR
110	REG1G1FSLDRN1L1KHD1P1L1BEPKSF1L1M1Y1P1F1R1T1O1K1Q1K1R1		Scen
97	REG1G1FSLDRN1L1KHD1P1L1BEPKSF1L1M1Y1P1F1R1T1O1K1Q1K1R1		Calin
104	REG1G1FSLDRN1L1KHD1P1L1BEPKSF1L1M1Y1P1F1R1T1O1K1Q1K1R1		Klack
108	REG1G1FSLDRN1L1KHD1P1L1BEPKSF1L1M1Y1P1F1R1T1O1K1Q1K1R1		Anid
85	REG1G1FSLDRN1L1P1A1S1M1L1BEPKSF1L1M1Y1P1F1R1T1O1K1Q1K1R1		Cpu

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Fig. 9b

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Fig. 10

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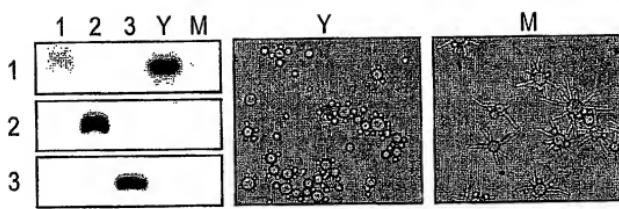
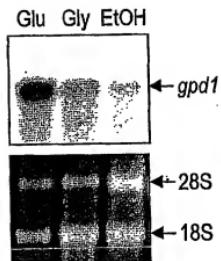
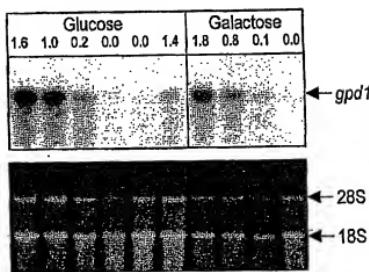


Fig. 11

Fig. 12a**Fig. 12b**

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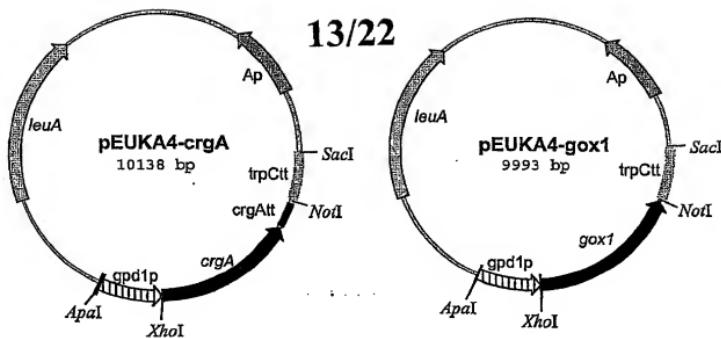


Fig. 13a

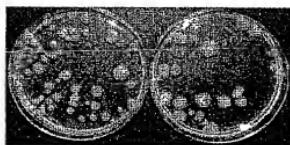


Fig. 13b



Fig. 13c

Fig. 14a

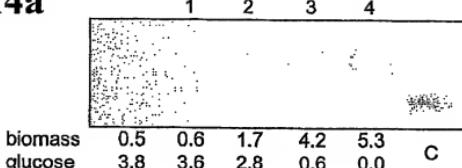


Fig. 14b

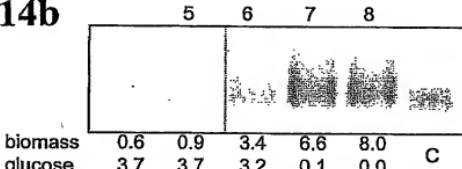


Fig. 14c

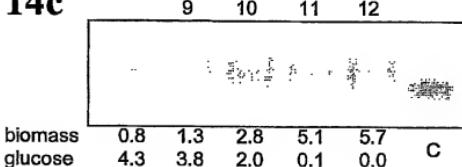


Fig. 14d

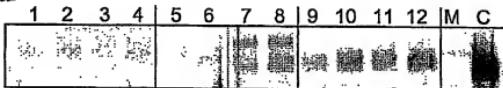


Fig. 14

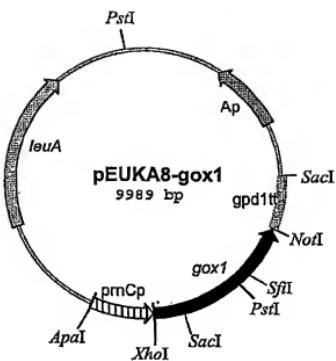


Fig. 15

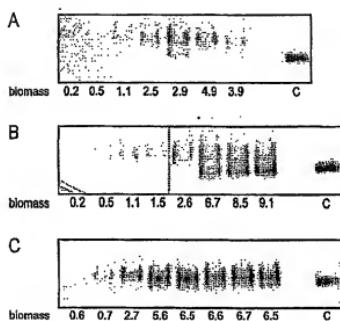


Fig. 16

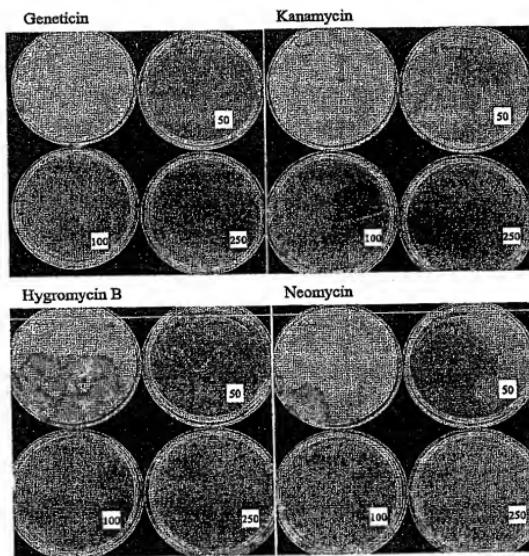


Fig. 17

Fig. 18a

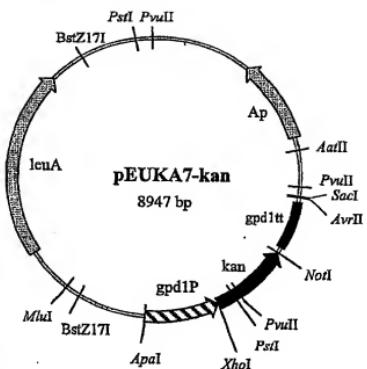


Fig. 18b

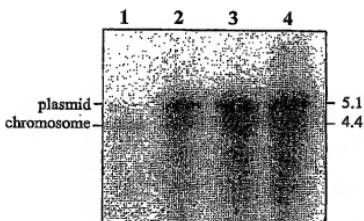
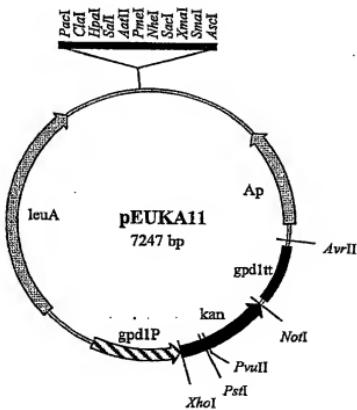
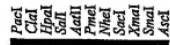


Fig. 18c



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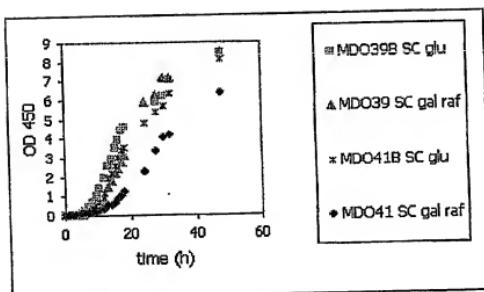


Fig. 19

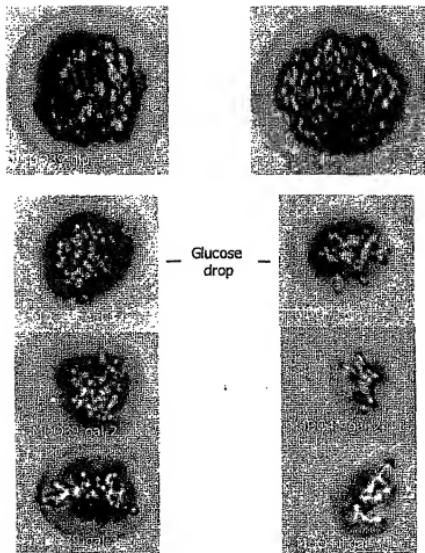


Fig. 20

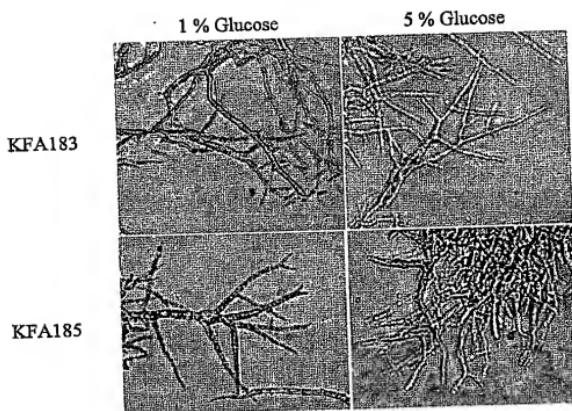


Fig. 21

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Fig. 22

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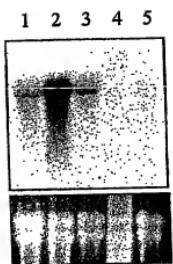


Fig. 23